

Silicon N Channel Power MOSFET

Description

The HXN0608 is n-channel power trench MOSFET with latest technology. So fast switching speed and low on-resistance. Usually used at power switching application . It is also intended for any applications with low gate drive requirements .

Product Summary

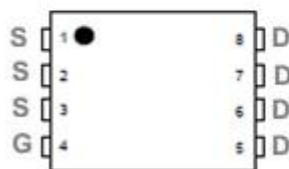
BVDS	RDSON	ID
60V	15mΩ	8A

Features

- Latest Trench Power MOSFET technology
- Low On-state Resistance
- High Current Density
- Low Gate Charge
- 100% UIS Test

Applications

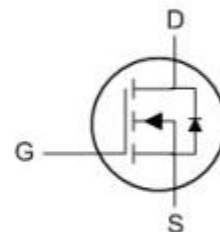
- Power Management
- Motor Driver



DFN5*6



SOP8



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1、 Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DS}	Drain-source voltage ($V_{GS} = 0$)	60	V
V_{GS}	Gate-source voltage	± 20	V
$I_D^{(1)}$	Drain current (continuous) at $TC = 25\text{ }^\circ\text{C}$	8	A
$I_{DM}^{(2)}$	Drain current (pulsed)	32	A
P_D	Power dissipation at $TC = 25\text{ }^\circ\text{C}$	2.0	W
$E_{AS}^{(3)}$	Single pulse avalanche energy	150	mJ
T_j	Operating junction temperature	-55 to 150	$^\circ\text{C}$

1. Current limited by package
2. Pulse width limited by safe operating area
3. Starting $T_j = 25\text{ }^\circ\text{C}$, $I_D = 18\text{A}$, $V_{DD} = 30\text{V}$, $L = 1\text{mH}$

2、 Thermal data

Symbol	Parameter	Min.	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		60		$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case		20		$^\circ\text{C}/\text{W}$

3、 Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown	$I_D = 250\text{ }\mu\text{A}$, $V_{GS} = 0$	60			V
I_{DSS}	Zero gate voltage drain	$V_{DS} = \text{Max rating}$			1	μA
I_{GSS}	Gate body leakage current	$V_{GS} = \pm 20\text{V}$			± 100	nA
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}$	1	1.3	2.5	V
$R_{DS(on)}$	Static drain-source on	$V_{GS} = 10\text{V}$, $I_D = 8\text{A}$		10	12	$\text{m}\Omega$
$R_{DS(on)}$	Static drain-source on	$V_{GS} = 4.5\text{V}$, $I_D = 6\text{A}$		12	15	$\text{m}\Omega$

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C_{iss}	Input capacitance	$V_{DS} = 25V$		3000		pF
C_{oss}	Output capacitance	$f = 1 \text{ MHz}$		230		pF
C_{rss}	Reverse transfer	$V_{GS} = 0$		170		pF
Q_g	Total gate charge	$V_{DD} = 30V$		30		nC
Q_{gs}	Gate-source charge	$I_D = 40A$		12		nC
Q_{gd}	Gate-drain charge	$V_{GS} = 10V$		9		nC
I_{SD}	Source-drain current				50	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)				200	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 8A, V_{GS} = 0$			1.2	V

1. Pulse width limited by safe operating area
2. Pulsed: pulse duration=300 μ s, duty cycle 1.5%